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TO: USPTO
Examiner: Dinh Thanh Le - Art Unit: 2816

FAX NO. 571-273-1745

FROM: J. Joel Justiss

RE: Serial No.: 10/033,525
Attorney Docket No.: R. PULLELA 4-4
Supplemental Amendment Under 37 C.F.R. § 1.111

DATE: September 16, 2005

PAGES: 8 (including cover page)

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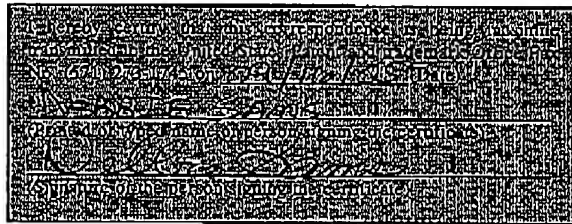
ATTORNEY DOCKET NO. R. PULLELA 4-4

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Rajasekhar Pullela, *et al.*
Serial No.: 10/033,525
Filed: December 28, 2001
Title: TRANS-ADMITTANCE TRANS-IMPEDANCE
LOGIC FOR INTEGRATED CIRCUITS
Grp./A.U.: 2816
Examiner: Dinh Thanh Le

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Sir:

SUPPLEMENTAL AMENDMENT UNDER 37 C.F.R. § 1.111

The Applicants have carefully considered this application in connection with the telephone conversation with the Examiner on September, 16, 2005, and respectfully request reconsideration of this application in view of the following amendment and remarks.